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In re Application of :

Serial No. : 10/688,023

Filed : October 16, 2003

Group Art Unit : 2818

Examiner :

Atty Docket : 5201-27700 / 03-1349



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Attached is a copy of the Official Filing Receipt received from the U.S. Patent and Trademark Office in connection with the above application for which issuance of a corrected Filing Receipt is respectfully requested.

There is an error with respect to the following data which was:

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<input type="checkbox"/> Applicant(s) name	Geeta Desai
<input type="checkbox"/> Applicant(s) address	
<input type="checkbox"/> Title of Application	Integrated NAND And Nor-Type Flash Memory Device And Method Of
<input checked="" type="checkbox"/> Filing Date	10/16/03
<input type="checkbox"/> Serial Number	10/688,023
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LSI Logic Corporation
1551 McCarthy Blvd., MS D-106
Milipitas, CA 95035
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Date: 02/11/04

Respectfully submitted,

Leo Peters

Reg. No. 33,562



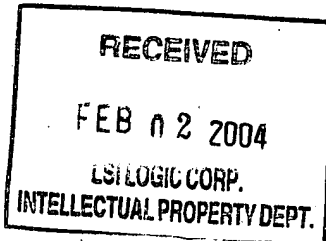
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Leo Peters
 LSI Logic Corporation
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OC000000011775207

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Applicant(s)

Geeta Desai, Saratoga, CA;
 Vijendra Kuroodi, Cupertino, CA;
 Remi Lenoir, Menlo Park, CA;

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 01/21/2004

Projected Publication Date: Request for Non-Publication Acknowledged

Non-Publication Request: Yes

Early Publication Request: No

Title

Integrated NAND and nor-type flash memory device and method of using the same

Preliminary Class

365



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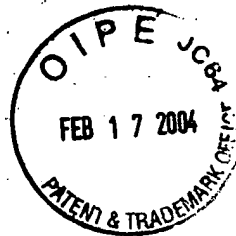
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<div style="text-align: center;"> Pamela Gerik</div>	

**INTEGRATED NAND AND NOR-TYPE FLASH MEMORY
DEVICE AND METHOD OF USING THE SAME**

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